

FEB 22 2005

PATENT APPLICATION
Attorney's Do. No. 2522-026
Client No. AW8116US/GJ

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: Ji-Young KIM and Hyoung-Sub KIM

Serial No. 10/649,262 Examiner: Lindsay Jr. Walter Lee

Filed: August 26, 2003 Group Art Unit: 2812

Confirmation No. 7198

For: INTEGRATION METHOD OF A SEMICONDUCTOR
DEVICE HAVING A RECESSED GATE ELECTRODEMail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RESPONSE TO RESTRICTION REQUIREMENT**

Responsive to the Restriction Requirement, dated February 4, 2005, Applicant hereby elects Group II (claims 1-13 and 19-25), without traverse.

Customer No. 20575

Respectfully submitted,

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Hosoon Lee
Limited Recognition Under 37 CFR § 10.9(b)MARGER, JOHNSON, & McCOLLOM, P.C.
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I hereby certify that this correspondence is being transmitted to the U.S. Patent and Trademark Office via facsimile number 703-872-9306, on February 22, 2005.



Li Mei Vermilya